

PPPL DESIGN REVIEW CHIT

WP # 1710 (ENG-032)CHIT # 1

COMPONENT/SUBSYSTEM/SYSTEM Fusion Source Profile Diagnostic

 PEER CDR PDR FDRCOGNIZANT DESIGN ENGINEER D. Darrow DATE OF REVIEW 2/15/11**SUBJECT: (CHECK AS APPLICABLE)** REQUIREMENTS ANALYSIS PERFORMANCE HARDWARE CONFIGURATION RELIABILITY/MAINTAINABILITY SAFETY SECURITY & SAFEGUARDS COST/SCHEDULE QUALITY**COMMENT/CONCERN/RECOMMENDATION**

Need to assess impacts of possible data storage requirements on MDS+ and disk usage

ORIGINATOR —

M. Bell

NAME/ORGANIZATION

REVIEW BOARD COMMENT/RECOMMENDATION

(Address technical, cost, and schedule impacts as appropriate. If CHIT is not adopted, provide technical reason - do not simply state "out-of-scope or N/A" without explaining.)

ACTION DARROW/BOEGLIN/VALENZUELA

 CONCUR DISAGREE OTHER

CHAIRPERSON

W. StenlundDATE: 2.15.11**COGNIZANT DESIGN ENGINEER'S RESPONSE/DISPOSITION:**

SIGNATURE _____ DATE: _____

RESPONSIBLE RLM REVIEW APPROVE COG DISPOSITION DISAPPROVE COG DISPOSITION

SIGNATURE _____ DATE: _____

COGNIZANT DESIGN ENGINEER CLOSE-OUT

Sign when action required by disposition is complete.

SIGNATURE _____ DATE: _____

PPPL DESIGN REVIEW CHIT

WP # 1710 (ENG-032)

CHIT # 2

COMPONENT/SUBSYSTEM/SYSTEM Fusion Source Profile Diagnostic_____

- PEER
- CDR
- PDR
- FDR

COGNIZANT DESIGN ENGINEER D. Darrow_____ DATE OF REVIEW 2/15/11

SUBJECT: (CHECK AS APPLICABLE)

- REQUIREMENTS
- ANALYSIS
- PERFORMANCE

- HARDWARE
- CONFIGURATION
- RELIABILITY/MAINTAINABILITY

- SAFETY
- SECURITY & SAFEGUARDS
- COST/SCHEDULE
- QUALITY

COMMENT/CONCERN/RECOMMENDATION

If a connector is needed at diode pins, it needs to be NSTX vacuum compatible cables too.

ORIGINATOR — M. Bell

NAME/ORGANIZATION

REVIEW BOARD COMMENT/RECOMMENDATION

(Address technical, cost, and schedule impacts as appropriate. If CHIT is not adopted, provide technical reason - do not simply state "out-of-scope or N/A" without explaining.)

Action ROQUEMORE / BOEGLIN / VALENZUELA

- CONCUR
- DISAGREE
- OTHER

CHAIRPERSON W. Blankenship DATE: 2.15.11

COGNIZANT DESIGN ENGINEER'S RESPONSE/DISPOSITION:

SIGNATURE _____ DATE: _____

RESPONSIBLE RLM REVIEW

- APPROVE COG DISPOSITION
- DISAPPROVE COG DISPOSITION

SIGNATURE _____ DATE: _____

COGNIZANT DESIGN ENGINEER CLOSE-OUT

Sign when action required by disposition is complete.

SIGNATURE _____ DATE: _____

PPPL DESIGN REVIEW CHIT

WP # 1710 (ENG-032)CHIT # 3

COMPONENT/SUBSYSTEM/SYSTEM Fusion Source Profile Diagnostic

 PEER CDR PDR FDRCOGNIZANT DESIGN ENGINEER D. Darrow DATE OF REVIEW 2/15/11**SUBJECT: (CHECK AS APPLICABLE)** REQUIREMENTS ANALYSIS PERFORMANCE HARDWARE CONFIGURATION RELIABILITY/MAINTAINABILITY SAFETY SECURITY & SAFEGUARDS COST/SCHEDULE QUALITY**COMMENT/CONCERN/RECOMMENDATION**

Need to have a method to measure and/or confirm the angles of the detector housing w.r.t. the mounting shaft.

ORIGINATOR

NAME/ORGANIZATION

M. Bell**REVIEW BOARD COMMENT/RECOMMENDATION**

(Address technical, cost, and schedule impacts as appropriate. If CHIT is not adopted, provide technical reason - do not simply state "out-of-scope or N/A" without explaining.)

ACTION BOEGLIN/VALENZUELA

 CONCUR DISAGREE OTHER

CHAIRPERSON

W. Blumberg

DATE:

2-15-11**COGNIZANT DESIGN ENGINEER'S RESPONSE/DISPOSITION:**

SIGNATURE _____

DATE: _____

RESPONSIBLE RLM REVIEW APPROVE COG DISPOSITION DISAPPROVE COG DISPOSITION

SIGNATURE _____

DATE: _____

COGNIZANT DESIGN ENGINEER CLOSE-OUT

Sign when action required by disposition is complete.

SIGNATURE _____

DATE: _____